

01/16/2007 TUE 15:59 [TX/RX NO 6363] 006

BOSCH GRAF VON STOSCH JEHL

PATENTANWALTSGESELLSCHAFT MBH

P.A. Bosch Graf von Stosch Jähle, Flüggenstraße 13, D-80639 München

Garlick, Harrison & Markison, LLP
2517 Improver Rd.
Spicewood, TX 78669
U.S.A.

Patentanwälte ⁽¹⁾
European Patent Attorneys ⁽²⁾
European Trade Mark and Design Attorneys ⁽³⁾

Dr. Matthias Bosch, Dipl.-Ing. ^{1, 2, 3, 4}
Dr. Andreas Graf v. Stosch LL.M., (Dipl.-Bioch. ^{1, 2, 3, 4}
Volker Jähle, Dipl.-Ing., (Dipl.-Kfm. ^{1, 2, 3, 4}
Albrecht Ritter, Dipl.-Ing. ^{1, 2, 3, 4}
Dr. Heiko Peters, Dipl.-Bioch. ^{1, 2, 3, 4}
Tilman Tarutis, Dipl.-Ing. ^{1, 2, 3, 4}
Dr. Martin Scharping, Dipl.-Bioch. ^{1, 2}

Sitz der Gesellschaft: München
Handelsregister: München HRR 153844
Geschäftsführer ⁽⁴⁾, Prokurist ⁽⁵⁾

Technical Consultant
Trinnie A. Kern, B.S.E.E., (MII)

Flüggenstraße 13
D-80639 München

Tel.: +49 - 89 - 18 92 78 0 info@bgsj.de
Fax: +49 - 89 - 18 92 78 - 88 www.bgsj.de

Date
December 19, 2006

Our reference
BPQ2P354EP/BO/bs

Your reference
BP3036

European Patent application 03022638.5-1247
Iterative metric updating when decoding LDPC (Low Density Parity Check) coded signals and
LDPC coded modulation signals
Broadcom Corporation

Dear Sirs,

Enclosed herewith, please find some documents for the IDS.

Very truly yours

BOSCH GRAF VON STOSCH JEHL
PATENTANWALTSGESELLSCHAFT MBH


Dr. Matthias Bosch
Patent Attorney

1/13/07
1/15/07

MÜNCHEN DÜSSELDORF ALICANTE

WinPSK Technical Reference Manual

by

Moe Wheatley, AE4JY
ae4jy@mindspring.com

Table of Contents

| | |
|---|-----------|
| 1. WINPSK OVERVIEW | 4 |
| 1.1. Introduction | 4 |
| 2. WINPSK SIGNAL GENERATION | 5 |
| 2.1. Block Diagram | 5 |
| 2.2. Input Characters | 5 |
| 2.3. Varicode Encoding | 5 |
| 2.4. BPSK Serialization | 7 |
| 2.5. QPSK Serialization | 7 |
| 2.5.1. ECC Encoding Method | 7 |
| 2.6. Differential Phase Shift encoding | 8 |
| 2.7. Wave Shaping and Carrier Generation | 9 |
| 2.8. Power Spectrum | 13 |
| 3. WINPSK SIGNAL DETECTION | 14 |
| 3.1. Block Diagram | 14 |
| 3.2. Soundcard Input | 15 |
| 3.3. Decimation by 2 | 15 |
| 3.4. Complex Mixer | 15 |
| 3.5. Decimation by 8 | 16 |
| 3.6. Matched Data Bit filter | 17 |
| 3.7. Frequency Error filter | 18 |
| 3.8. AGC | 19 |
| 3.9. Frequency Error Detection/Correction | 20 |
| 3.10. Symbol Synchronization | 24 |
| 3.11. Squelch Function | 25 |
| 3.12. Symbol Decoding | 31 |
| 3.12.1. BPSK | 32 |
| 3.12.1.1. Maximum Likelihood Detector | 32 |
| 3.12.2. QPSK | 34 |
| 3.12.2.1. Maximum Likelihood example | 34 |

| | | |
|-----------|--|----|
| 3.12.2.2. | Soft Viterbi Decoder..... | 35 |
| 3.13. | Display Signals | 38 |
| 3.13.1. | FFT for Spectrum Display | 38 |
| 3.13.2. | Vector Display | 38 |
| 3.13.3. | Input Signal..... | 38 |
| 3.13.4. | Sync histogram..... | 38 |
| 4. | WINDOWS PROGRAM IMPLEMENTATION..... | 39 |
| 4.1. | PC/Windows Implementation Issues | 39 |
| 4.2. | Real Time Considerations..... | 39 |
| 4.3. | Float vs. Integer Implementation..... | 39 |
| 4.4. | PC Soundcard Settings | 40 |
| 4.5. | Program Structure..... | 41 |
| 4.5.1. | Hierarchy Diagram..... | 41 |
| 4.5.2. | Class Descriptions..... | 42 |
| 4.6. | Miscellaneous Software issues..... | 44 |
| 4.6.1. | FIR Filter implementation..... | 44 |
| 4.6.2. | Inter-Class Communication | 45 |
| 4.6.3. | Processor Loading..... | 45 |
| | PROBLEMS/BUGS/ISSUES..... | 46 |
| 5. | REFERENCES:..... | 47 |

1. WinPSK Overview

1.1. Introduction

PSK31 is an amateur radio communications mode introduced by Peter Martinez, G3PLX, that uses phase modulation and special character coding. It provides robust narrow bandwidth keyboard Chat type communications between two or more stations.

This document was written to describe some of the internal workings of the WinPSK program that was developed as a result of my experimenting with DSP on a PC soundcard. Previously, experimenting with DSP was achieved using evaluation boards from various DSP chip manufacturers. Programming these boards was tedious due to their assembly language and fixed-point number representation. Trying to learn the basics of DSP often got lost in the details of programming and debugging these specialty processors.

Beginning with the Intel 486 and subsequent Pentium class processors being used in the popular desktop PC platform, the processing power has increased to the point where real time signal processing can now be done using floating point arithmetic and a PC soundcard for analog I/O. The amateur radio community has benefited from these advances with PC Soundcard based applications for SSTV, RTTY, and more recently, PSK31.

My interest in all this was in learning how to develop and program various DSP communications algorithms using a standard Windows[®] based PC platform. It is from these experiments that WinPSK evolved from basically a DSP test bed to a simple functioning program for PSK31. This paper describes in some detail the basic design decisions that were made during this learning process that led to the final program. It is not meant as a definitive reference on PSK31 implementation but just an engineering notebook describing this program. Perhaps others can build on some of the information here to improve this program as well as be motivated to experiment with new modes.

The basic goal was to write a working PSK31 interface program from scratch. Unlike some other HF modes, Peter Martinez has made available very complete specifications for this mode^{2,3}. Also his Windows program "PSK31" provided an excellent reference program for verifying and testing various algorithms.

"WinPSK User Guide" is a separate document that describes the user operation of the program. This document only describes the inner workings of the program.

The program will be described in sections starting with signal generation then followed by the reception algorithms. Finally, the overall software architecture and miscellaneous issues will be discussed.